

This is the block diagram for the sync circuitry.

FIG. 1

Detector Connecting Diagram

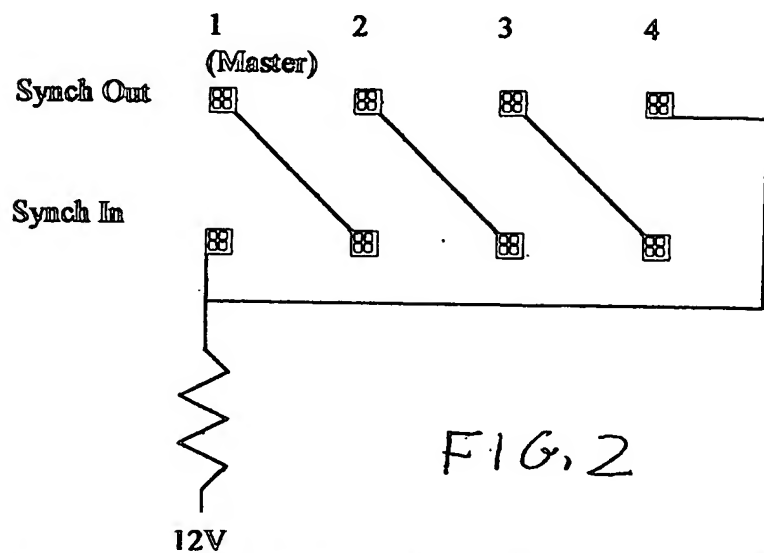


FIG. 2

Series Synch Operation Diagram

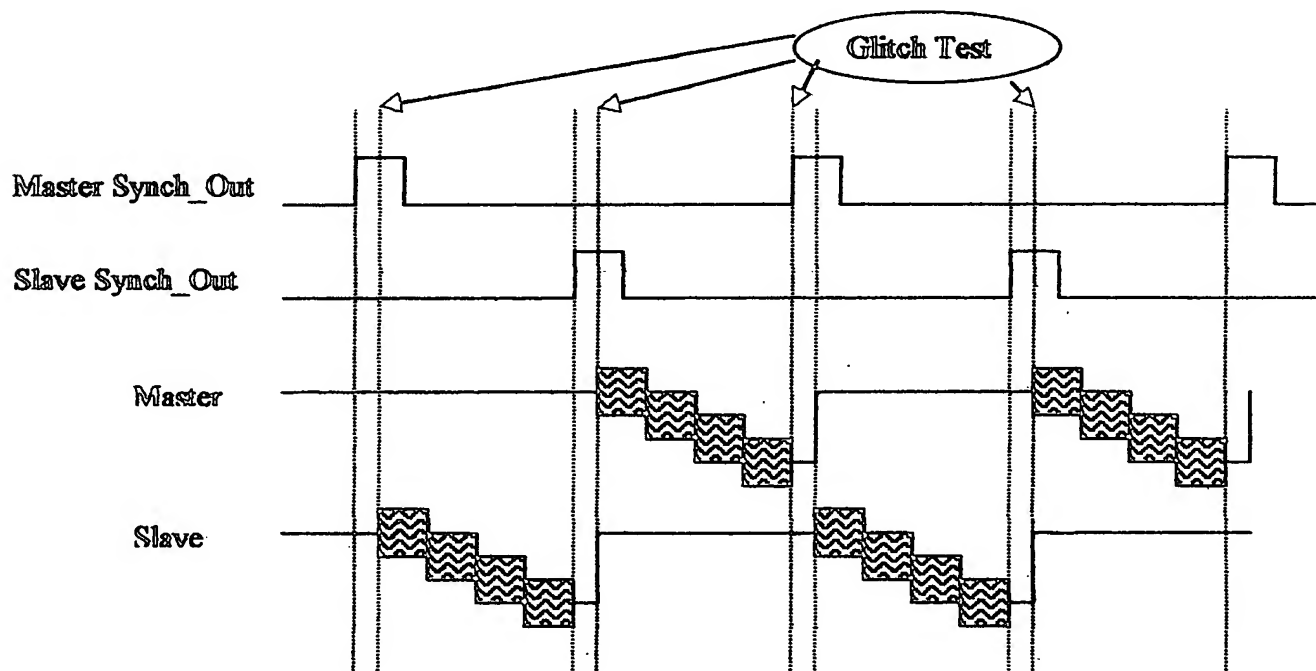


FIG. 3

Detector Connecting Diagram

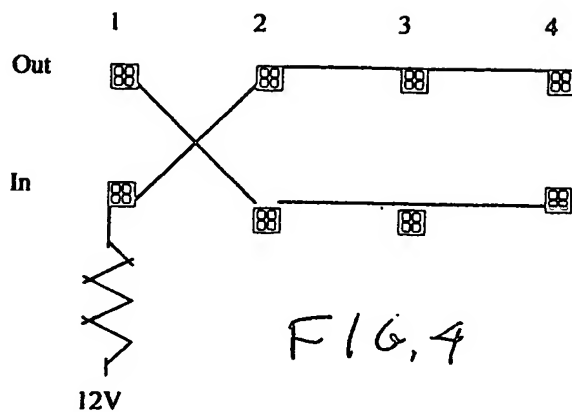


FIG. 4

Parallel Synch Operation Diagram

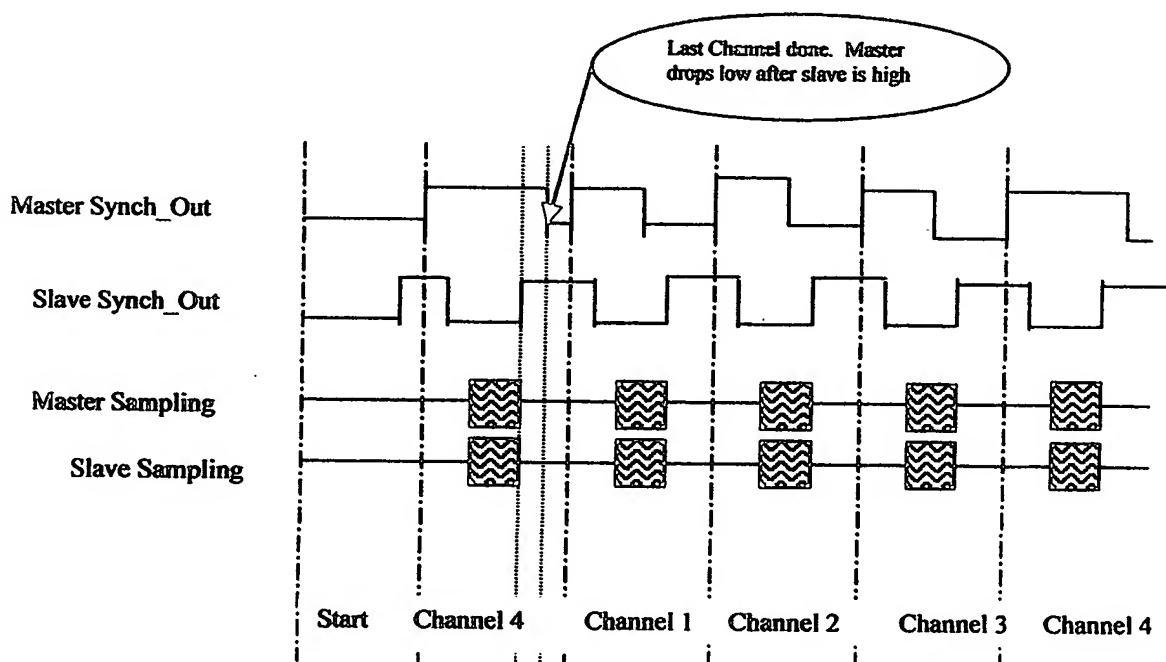


FIG. 5

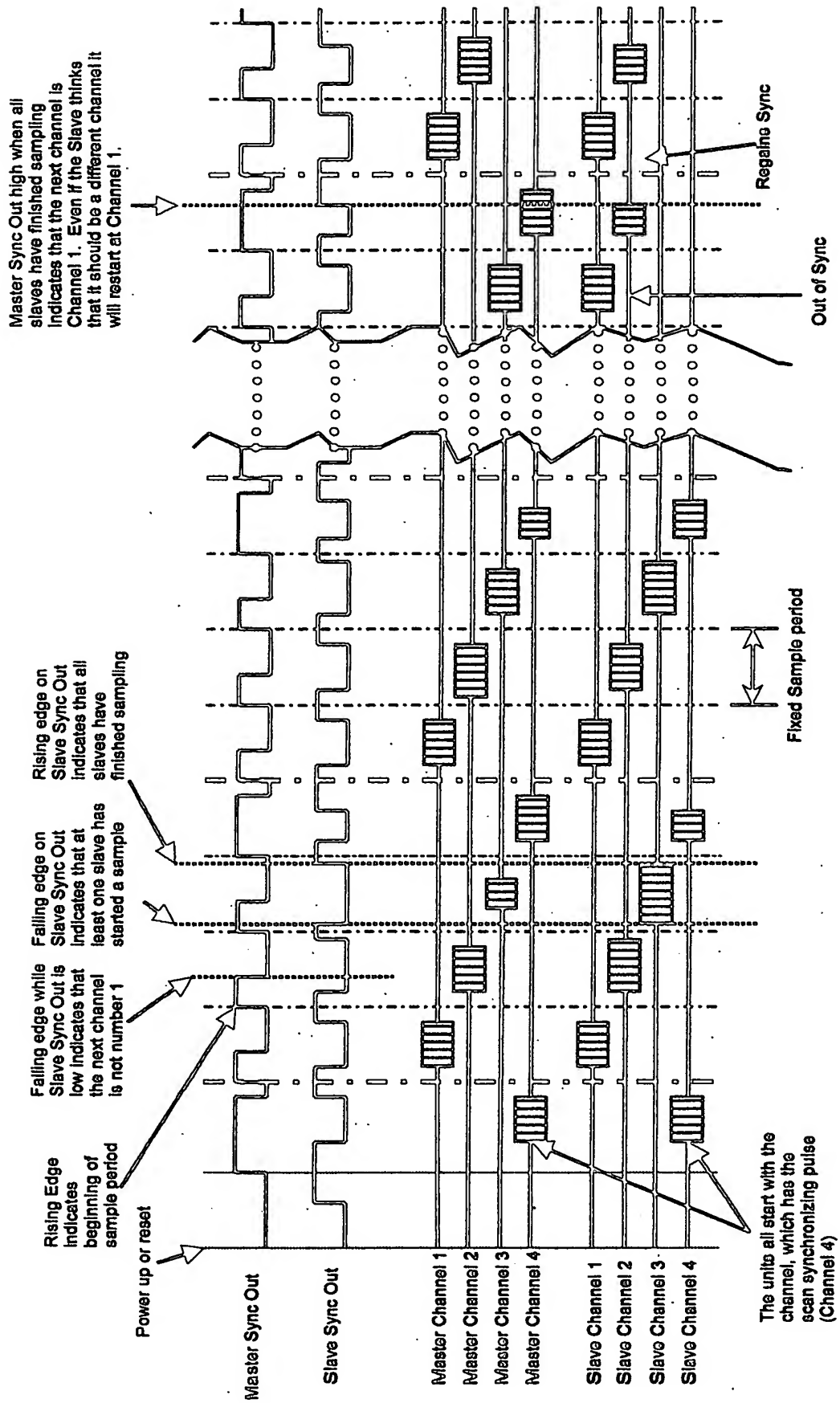


FIG. 6

The units all start with the channel, which has the scan synchronizing pulse (Channel 4)